DETAILED ACTION

1. This action is responsive to Applicant's reply filed on September 9, 2011.

2. Claims 1-20 have been examined.

Response to Arguments

3. Remarks, page 8, "an adjustment device that adjusts a control device".

In response to applicant's argument that the references fail to show certain features of applicant's invention, it is noted that the features upon which applicant relies (i.e., *separate/distinct devices, two computers*) are not recited in the rejected claim(s). Although the claims are interpreted in light of the specification, limitations from the specification are not read into the claims. See In re Van Geuns, 988 F.2d 1181, 26 USPQ2d 1057 (Fed. Cir. 1993).

The plain language of the claims recites:

> 1. (Previously Presented) An adjustment device for adjusting at least one control device with at least one control device microcontroller and with at least one control device debug interface, comprising:

As claimed above, the control device merely includes a microcontroller + debug interface. Accordingly, the adjustment device can be either a host of (the microcontroller + debug interface), i.e., a host of the control device, or an external/separate device.

In the instant case, Bates teaches Computer System 10 (adjustment device including OS 18, Compiler 21, Programming Environment 22, Break Point Manager 30) hosts (Processor 12 + Debug User Interface 24 = = control device).

Claim Rejections – 35 USC §102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in (1) an application for patent, published under section 122(b), by another filed in the United States before the invention by the applicant for patent or (2) a patent granted on an application for patent by another filed in the United States before the invention by the applicant for patent, except that an international application filed under the treaty defined in section 351(a) shall have the effects for purposes of this subsection of an application filed in the United States only if the international application designated the United States and was published under Article 21(2) of such treaty in the English language.

5. Claims 1-20 are rejected under 35 U.S.C. 102(e) as being anticipated by Bates (US Patent Publication No. 2004/0054944).

Claim 1:

Bates discloses *an adjustment device for adjusting at least one control device with at least one control device microcontroller and with at least one control device debug interface comprising:*

the adjustment device (FIG. 1, Computer System 10, 0033, 0035, 0037-0039);

the control device (FIG. 1, Processor 12 and Debug User Interface 24 and related text)

at least one programmable unit (FIG. 1, OS 18 or Compiler 21)*;*

at least one data transmission interface for connecting the adjustment device to an operating unit (0012, 0019-0022, 0039, 0044-0049, FIG. 1, Terminal Interface 40 connects 10 to 42; Network Interface 44 connects 10 to 46)*; and*

at least one adjustment device debug interface for connecting the adjustment device to the control device debug interface of the control device (0015, 0022-0026, 0040, FIG. 1, Programming Environment as an interface for connecting Computer System 10 to Debug User Interface 24, bus 14 as an interface for connecting 10 to 24; FIG. 2, Breakpoint Manager 30 as an interface for connecting 10 to 24)*; and*

characterized by the adjustment device comprises in addition at least one memory for at least one address list and at least one data list (0016, 0040-0043, 0048-0051)*,*

where the addresses stored in the address list denote memory locations in the address space of the control device microcontroller (0025, 0033, 0044-0047, 0051-0053) *and*

where with the use of the adjustment device debug interface data from the memory locations which are in the address space of the control device microcontroller (0014-0018, 0039, 0045-0048) *and*

which are determined by the contents of the address list can be read and stored in the data list and/or the data stored in the data list can be stored at the memory locations (0018-0022, 0033-0036, 0053)

which are in the address space of the control device microcontroller and which are determined by the contents of the address list (0013-0016, 0039-0043, 0050-0053).

Claim 2:

Bates discloses the *adjustment device according to claim 1, characterized by the wherein the programmable unit comprises the adjustment device debug interface* (0015, 0022-0026, 0040).

Claim 3:

Bates discloses the *adjustment device according to Claim 1 the programmable unit is a programmable logic chip* (0012, 0019-0022, 0039, 0044-0049).

Claim 4:

Bates discloses the *adjustment device according to Claim, the memory for the address list and for the data list is provided in the programmable unit* (0025, 0033, 0044-0047, 0051-0053).

Claim 5:

Bates discloses the *adjustment device according to Claim 1, wherein the programmable unit comprises a list application unit and by activation of the list application unit* (0015, 0022-0026, 0040)

the list application unit automatically carries out either the calling of the data from the memory locations in the address space of the control device microcontroller and given in the address list and the storing of the called data in the data list or the writing of the data stored in the data list (0012, 0019-0022, 0039, 0044-0049)

into the memory locations in the address space of the control device microcontroller and determined by the contents of the address list (0016, 0040-0043, 0048-0051).

Claim 6:

Bates discloses the *adjustment device according to Claim 5, wherein in the case of several address lists and/or several data lists by issuing priorities for the address lists and/or data lists a processing order can be determined by the list application unit* (0014-0018, 0039, 0045-0048).

Claim 7:

Bates discloses the *adjustment device according to Claim 5, claim 5 wherein in the case of several address lists and/or several data lists a subset of address lists and/or a subset of data lists can be determined which is processed by the list application unit* (0018-0022, 0033-0036, 0053).

Claim 8:

Bates discloses the *adjustment device according to Claim 1, wherein the programmable unit comprises an individual application unit with which any memory locations in the address space of the control device microcontroller* (0025, 0033, 0044-0047, 0051-0053)

can be read out and/or with which a value can be stored in any memory location in the address space of the control device microcontroller (0014-0018, 0039, 0045-0048).

Claim 9:

Bates discloses the *adjustment device according to Claim 1, wherein the programmable unit comprises a tool interface unit for connecting at least one external device to the adjustment device* (0016, 0040-0043, 0048-0051).

Claim 10:

Bates discloses the *adjustment device according to Claim 1, wherein the programmable unit comprises a bypass unit with an associated single-port or dual-port bypass memory* (0025, 0033, 0044-0047, 0051-0053)

an associated bypass interface for connecting the bypass unit and the bypass memory to an external simulation unit where data can be exchanged (0012, 0019-0022, 0039, 0044-0049)

between the control device and the simulation unit with the use of the bypass memory and the bypass unit reading and writing bi-directionally (0018-0022, 0033-0036, 0053).

Claim 11:

Bates discloses the *adjustment device according to claim 10, wherein the programmable unit comprises the bypass interface where the bypass interface uses a serial data transmission and is embodied as an interface* (0014-0018, 0039, 0045-0048).

Claim 12:

Bates discloses the *adjustment device according to Claim 1, wherein the programmable unit comprises a prioritization and arbitration unit where priorities can be assigned to the various units of the programmable unit via the prioritization* (0016, 0040-0043, 0048-0051) *and*

arbitration unit and the prioritization and arbitration unit determines, with the aid of the priorities assigned to the various units the order of execution (0013-0016, 0039-0043, 0050-0053)

by activation of the various units among themselves and establishes a data connection between the unit activated in each case and the control device (0012, 0019-0022, 0039, 0044-0049).

Claim 13:

Bates discloses the *adjustment device according to claim 12, wherein the priority of the bypass unit is higher than the priority of the list application unit and/or that the priority of the list application unit* (0018-0022, 0033-0036, 0053)

is higher than the priority of the individual value application unit and/or that the priority of the individual value application unit is higher than the priority of the tool interface unit (0015, 0022-0026, 0040).

Claim 14:

Bates discloses the *adjustment device according to claim 12, wherein the priority of the tool interface unit is higher than all the other units* (0025, 0033, 0044-0047, 0051-0053).

Claim 15:

Bates discloses the *adjustment device according to Claim 1, wherein, the adjustment device comprises a coordination unit which is connected via a coordination interface to one or more of the units of the programmable unit* (0013-0016, 0039-0043, 0050-0053) *and/or*

via the data transmission interface to the operating computer and/or via the bypass interface to the simulation unit and/or to the bypass memory (0014-0018, 0039, 0045-0048).

Claim 16:

Bates discloses the *adjustment device according to claim 15, wherein the coordination unit directs data or instructions coming from the operating computer and/or*

from the simulation unit to the addressed units the programmable unit for further processing and/or transmits the data coming from a unit (0016, 0040-0043, 0048-0051).

Claim 17:

Bates discloses the *adjustment device according to Claim 15, wherein the coordination unit provides received data with a time stamp, data which are transmitted to the operating unit* (0025, 0033, 0044-0047, 0051-0053).

Claim 18:

Bates discloses the *adjustment device according to Claim 15, wherein the coordination unit interprets configuration instructions coming from the operating unit and/or from the simulation unit and configures the adjustment device accordingly* (0012, 0019-0022, 0039, 0044-0049).

Claim 19:

Bates discloses the *adjustment device according that Claim 15, wherein the coordination unit registers external trigger signals and/or internal trigger signals and activates corresponding units of the programmable unit* (0016, 0040-0043, 0048-0051).

Claim 20:

Bates discloses the *adjustment device according to Claim 15, wherein the coordination unit is located in a separate computer unit outside of the programmable unit , in a microcontroller, by in a programmable logic chip, or is formed as a part of the programmable unit* (0015, 0022-0026, 0040).

Conclusion

6. **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within

TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

7. Any inquiry concerning this communication should be directed to examiner Thuy (Twee) Dao, whose telephone/fax numbers are (571) 272 8570 and (571) 273 8570, respectively. The examiner can normally be reached on every Tuesday, Thursday, and Friday from 6:00AM to 6:00PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tuan Q. Dam, can be reached at (571) 272 3695.

Any inquiry of a general nature of relating to the status of this application or proceeding should be directed to the TC 2100 Group receptionist whose telephone number is (571) 272 2100.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

/Thuy Dao/ (Twee)
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